Notice of Allowability	Application No.	Applicant(s)
	10/695,458	AMIRICHIMEH ET AL.
	Examiner	Art Unit
	Raj K. Jain	2616
	rkaj rk. Jain	2010
- The MAILING DATE of this communication app All claims being allowable, PROSECUTION DN THE MERTIS S herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this ap or other appropriate communication IGHTS. This application is subject t	plication. If not included n will be mailed in due course. THIS
1. This communication is responsive to <u>10/30/07</u> .		
2. The allowed claim(s) is/are <u>1-5, 7-13, 17-25, 29-35, 37-43</u>	renumbered 1-35.	
Acknowledgment is made of a claim for foreign priority ut     a) □ All b) □ Some* c) □ None of the:	nder 35 U.S.C. § 119(a)-(d) or (f).	
Certified copies of the priority documents have been received.		
Certified copies of the priority documents have been received in Application No		
3. Copies of the certified copies of the priority documents have been received in this national stage application from the		
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		complying with the requirements
4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give		
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.		
(a) 🔲 including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached		
1) Thereto or 2) to Paper No./Mail Date		
<ul> <li>(b) including changes required by the attached Examiner's Paper No./Mail Date</li> </ul>	s Amendment / Comment or in the C	Office action of
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t	.84(c)) should be written on the drawing the header according to 37 CFR 1.121(	ngs in the front (not the back) of d).
6. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT		
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5. ☐ Notice of Informal P	data-t Application
Notice of Practices Cited (PTO-692)     Notice of Draftperson's Patent Drawing Review (PTO-948)		
2. Involce of Dialiperson's Faterit Diawing Review (F10-946)	Paper No./Mail Dat	
3. ☑ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date. 2013 (0 30 07	<ol><li>Examiner's Amendr</li></ol>	ment/Comment
4. Examiner's Comment Regarding Requirement for Deposit	8. X Examiner's Stateme	ent of Reasons for Allowance
of Biological Material	9.  Other	

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## DETAILED ACTION

## General Remarks

Examiner acknowledges and approves receipt of thirty sheets of Replacement Drawings with Figures 1, 2, 3A- 3D, 4-12, 13A, 13B, 14, 15A, 15B, 16-20, 21A, 21B, and 22-29 corresponding to the subject application. The Replacement drawings have been entered.

## Allowable Subject Matter

The following is an examiner's statement of reasons for allowance:

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Claims 1, 7, 17-19, 30, 41 and 43 are allowed.

The prior art discloses a network with plurality of cross link multiplexers, wherein the multiplexers have origin and destination ports for transceiving data signals accordingly. The multiplexers further have a plurality of interconnects to couple the adjacent cross link multiplexers.

The prior art however fails to disclose wherein a first interconnect is configured to convey a first bit of a number of bits of a data signal and a second interconnect is configured to convey a second bit of said number of bits of said data signal and said first bit remains substantially synchronized with said second bit.

Claim 13 is allowed.

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The prior art discloses a network with plurality of cross link multiplexers, wherein the multiplexers have origin and destination ports for transceiving data signals accordingly. The multiplexers further have a plurality of interconnects to couple the adjacent cross link multiplexers.

The prior art however fails to disclose wherein a first cross link multiplexer is configured to convey a data signal toward a second cross link multiplexer in a first direction via first set of interconnects coupled to a third cross link multiplexer of said plurality of cross link multiplexers and said first cross link multiplexer is configured to convey said data signal toward said second cross link multiplexer in a second direction via second set of interconnects of said plurality of interconnects coupled other than to said third cross link multiplexer.

Claim 24 is allowed

The prior art discloses a network with plurality of cross link multiplexers, wherein the multiplexers have origin and destination ports for transceiving data signals accordingly. The multiplexers further have a plurality of interconnects to couple the adjacent cross link multiplexers.

The prior art however fails to disclose wherein a first interconnect of a plurality of interconnects is used to convey a first bit of the data signal from the first cross link multiplexer to the second cross link multiplexer and a second interconnect of the plurality of interconnects is used to convey a second bit of the data signal from the first cross link multiplexer to the second cross link multiplexer.

Claim 32 is allowed.

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The prior art discloses a network with plurality of cross link multiplexers, wherein the multiplexers have origin and destination ports for transceiving data signals accordingly. The cross link multiplexers further have format conversion capability from one format to another format.

The prior art however fails to disclose wherein the first format is one of a 10 Gigabit Media Independent Interface protocol, a 10 Gigabit Attachment Unit Interface protocol, and a Converged Data Link protocol, the second format is one of the 10 Gigabit Media Independent Interface protocol, the 10 Gigabit Attachment Unit Interface protocol, and the Converged Data Link protocol, and the second format is different from the first format.

Claims 37 and 39, are allowed.

The prior art discloses a network with plurality of cross link multiplexers, wherein the multiplexers have origin and destination ports for transceiving data signals accordingly.

The prior art however fails to disclose cross link multiplexers synchronizing wherein said synchronizing comprises the step of conveying each bit of a number of bits through a corresponding delay flip-flop.

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Raj K. Jain whose telephone number is 571-272-3145. The examiner can normally be reached on M-F 8-5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chi Pham can be reached on 571-272-3179. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Raj K. Jain /Raj K. Jain/ Art Unit 2616

November 28, 2007

CHI PHAM

ON PATENT EXAMINER